



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT #66
S. 300d

RECEIVED
5/27/03
MAY 06 2003

Applicant(s): Brian A. Petersen and Mark A. Ross
Assignee: Cisco Technology, Inc.
Title: Network Processor System Including a Central Processor and at least one Peripheral Processor
Serial No.: 09/469,409 Filing Date: December 22, 1999
Examiner: Melvin H. Pollack Group Art Unit: 2142
Docket No.: CIS0042US

Technology Center 2100
amdt/B

Austin, Texas
April 28, 2003

Box: AF
COMMISSIONER FOR PATENTS
Washington, D. C. 20231

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the Final Office Action dated January 28, 2003, having a shortened statutory period expiring April 28, 2003. Further examination and reconsideration are respectfully requested in view of the remarks set forth below.

AMENDMENTS

In the Claims

In accordance with 37 CFR § 1.121, the following is a "clean version" of the entire set of pending claims.

1. A method of packet processing comprising:
parsing a packet, said packet having a header portion, to determine a vector;
coordinating processing using said vector;

Sub
B2